

DF Series Fast PIN Diode Drivers with TTL Logic Decoding

DESCRIPTION

High speed PIN drivers with Binary TTL inputs and Enable/Disable pins to greatly simplify system requirements. Logic inputs select one-of-n outputs to go negative, while all other outputs remain positive.

Logic testpoints are accessible which allow the user to configure the device either as a (0 to 4) or as a (1 to 5) Binary decoder.

Each output also has a testpoint which allows for tailoring of output current levels and spike currents for specific applications.

A wide variety of dual-output decoded PIN drivers are available for use in switched filter banks as 9000-series devices. Contact the factory for specifications and recommendations.

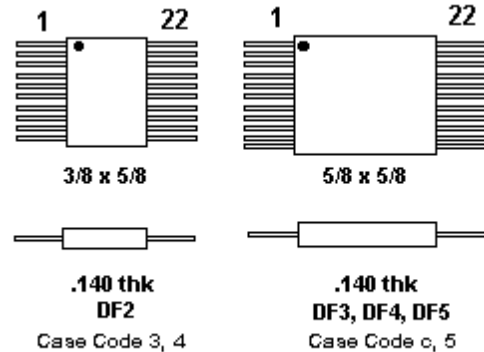
Higher voltage decoded fast PIN drivers (to 100 Volts) are described in Note # SD103.

FEATURES

- Low Quiescent Current, Stable vs VEE
- Dual Enable pins allow easy expansion
- Internal .01 μ F Bypass Capacitors
- Selectable Coding, 0-> (n-1) OR 1-> n
- Fast - 20 nsec Delay

PARAMETER	SYMBOL	MIN	TYP	MAX	UNITS
Pos. Bias Voltage	V+	4.5	5	7	V
Neg. Bias Voltage	VEE	-2	-5	-16	V
TTL Input Current -	I _{ttl0}	--	--	1.8	mA
TTL Input Current +	I _{ttl1}	--	--	.2	mA
Switching Speed	T _{sw}	--	14	20	nsec
Pos. Supply (no load) Total	I _{q+}	--	50	95	mA
Neg. Suppl (no load) Per Chan.	I _{q-}	--	3	6	mA

OUTLINES



LOGIC

E	\bar{E}	OUTPUTS	For Binary decoding starting at TTL 000 connect INPUT TP to LOGIC 0 pin. To decode starting at TTL 00, connect INPUT TP to LOGIC N pin.
0 or 1	0	ALL POS	
1	0 OR 1	ALL POS	
0	1	DECODING	

PIN CONNECTIONS

PIN	DF2	DF3	DF4	DF5
1	+5V	A	A	A
2	A	B	B	B
3	B	NC	C	C
4	E	E	E	E
5	E	E	E	E
6	GND	GND	GND	GND
7	TP1	NC	NC	TP4
8	OUT 1	NC	NC	OUT 4
9	Input TP	NC	TP3	TP3
10	TP0/2	NC	OUT 3	OUT 3
11	OUT 0/2	VEE	VEE	VEE
12	VEE	+5V	+5V	+5V
13	NC	NC	NC	NC
14	NC	TP2	TPs	TP2
15	NC	OUT 2	OUT 2	OUT 2
16	NC	TP1	TP1	TP1
17	NC	OUT 2	OUT 1	OUT 1
18	NC	OUT 0/3	OUT 0/4	OUT 0/5
19	NC	TPO/2	TPO/4	TPO/5
20	NC	Input N	Input N	Input N
21	Logic2	Logic3	Logic4	Logic5
22	Logic0	Logic0	Logic0	Logic0