## Impellimax

## XS Series PIN Drivers with X-OR (Mode Control) Function

## DESCRIPTION

## OUTLINES

Moderate speed PIN drivers with TTL mode control inputs. Outputs are positive when both inputs are logical complements.

XS Series drivers are reverse-bias protected and are rated for positive over-voltage to +5.5 V . All TTL inputs are ESD protected. Both bias supplies have internal $.01 \mu \mathrm{~F}$ bypass capacitors.

These drivers are guaranteed for stability when operating with any negative supply voltage from -2 V to -12 V (A voltage code) or -2 to -15 V (B voltage code).

## FEATURES

- Reverse Bias Protected
- Low Quiescent Current, Stable vs VEE
- Small Size: XS1, XS2 can be SIP XS2, XS3, XS4 are $3 / 8$ by 5/8
- Inverting and Noninverting Modes
- Fast - 30 nsec Typical Delay

| PARAMETER | SYMBOL | MIN | TYP | MAX | UNITS |
| :---: | :---: | :---: | :---: | :---: | :---: |
| Pos. Bias |  |  |  |  |  |
| Voltage | V+ | 4.5 | 5 | 7 | V |
| Neg. Bias Voltage | V- | -2 | -5 | - 16 | V |
| TTL Input | I- |  |  |  |  |
| Current - |  | -- | 0.8 | 1.6 | mA |
| TTL Input Current + | $\begin{aligned} & \hline \mathrm{I}+ \\ & \mathrm{t} \mid 1 \\ & \hline \end{aligned}$ | -- | -- | 40 | uA |
| Switching Speed | T sw | -- | 30 | 70 | nsec |
| Pos. Supply (no load) per Chan. | $1 q+$ | -- | 3 | 8 | mA |
| Neg Supply (no load) per Chan. | lq - | -- | 3 | 8 | mA |



LOGIC

| INA | INB | OUTPUT |
| :--- | :--- | :--- |
| 0 | If either INA or INB are |  |


| 0 | 0 | - |
| :--- | :--- | :--- |
| 0 | 1 | + |
| 1 | 0 | + |
| 1 | 1 | - | connected to ground, then the output will be noninverting relative to pulses input to the remaining input. Allowing either INA or INB to 'float' Hi , or connecting INA or INB to +5 V causes the output to be inverting relative to the remaining input.

## PIN CONNECTIONS

| PIN | XS1 | XS2 | XS3 | XS4 | XS5 | XS6 |
| :--- | :--- | :--- | :--- | :--- | :--- | :--- |
| $\mathbf{1}$ | VEE | $+5 v$ | $+5 v$ | $+5 v$ | $+5 v$ | $+5 v$ |
| $\mathbf{2}$ | Out | IN1A | IN1A | IN1A | Out 1 | Out 1 |
| $\mathbf{3}$ | Gnd | IN1B | IN1B | IN1B | IN1A | IN1A |
| $\mathbf{4}$ | INA | IN2A | IN2A | IN2A | IN1B | IN1B |
| $\mathbf{5}$ | INB | IN2B | IN2B | IN2B | Out 2 | Out 2 |
| $\mathbf{6}$ | NC | NC | NC | NC | IN2A | IN2A |
| $\mathbf{7}$ | +5v | Out 1 | Out 1 | Out 1 | IN2B | IN2B |
| $\mathbf{8}$ | NC | NC | NC | NC | Out 3 | Out 3 |
| $\mathbf{9}$ | NC | Out 2 | Out 2 | Out 2 | IN3A | IN3A |
| $\mathbf{1 0}$ | NC | Gnd | NC | NC | IN3B | IN3B |
| $\mathbf{1 1}$ | NC | VEE | VEE | VEE | Gnd | Gnd |
| $\mathbf{1 2}$ | NC | NC | Gnd | Gnd | VEE | VEE |
| $\mathbf{1 3}$ | NC | NC | NC | NC | NC | Out 6 |
| $\mathbf{1 4}$ | NC | NC | NC | Out 4 | NC | IN6A |
| $\mathbf{1 5}$ | NC | NC | NC | NC | NC | IN6B |
| $\mathbf{1 6}$ | NC | NC | Out 3 | Out 3 | Out 5 | Out 5 |
| $\mathbf{1 7}$ | NC | NC | NC | NC | IN5A | IN5A |
| $\mathbf{1 8}$ | NC | NC | NC | IN4B | IN5B | IN5B |
| $\mathbf{1 9}$ | NC | NC | NC | IN4A | Out 4 | Out 4 |
| $\mathbf{2 0}$ | NC | NC | IN3B | IN3B | IN4A | IN4A |
| $\mathbf{2 1}$ | NC | NC | IN3A | IN3A | IN4B | IN4B |
| $\mathbf{2 2 ~}$ | NC | NC | Gnd | Gnd | Gnd | Gnd |

