

| PIN | CONNECTION | PIN | CONNECTION |
| :---: | :---: | :---: | :---: |
| 1 | $-100 V$ | 22 | NC |
| 2 | Output 1 | 21 | NC |
| 3 | Dir 1 | 20 | Rext/Cext 1 |
| 4 | In 1 | 19 | NC |
| 5 | Mode 1 | 18 | NC |
| 6 | Mode 2 | 17 | NC |
| 7 | In 2 | 16 | NC |
| 8 | Dir 2 | 15 | Rext/Cext 2 |
| 9 | Output 2 | 14 | NC |
| 10 | $+5 V+/-.5 V$ | 13 | NC |
| 11 |  | 12 |  |

Notes:

1) Steady-state output current capability is + 100 mA nominal into a cathodegrounded PIN diode.
2) Switching speed is 25 nsec max loaded with a shunt 1N914 diode and 100 pF .
3) Unit is output short-circuit protected. Negative supply range is from -20V to -100 V maximum. +5 V input is not over-voltage protected.
4) Channels are independent and provide TTL Exclusive-Nor logic function. Inputs are inverting when mode is held low.
5) Connect 22 K parallel .01 uF (typical values) between Rext/Cext and output.

| Tolerances Except as Noted$\begin{aligned} & x=+/-\quad .05 \\ & x x=+/- .01 \\ & . x x=+/ .005 \\ & \text { Dimensions } \end{aligned}$in inches | Revisions |  |  |  | Impelimax |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | $\begin{array}{\|l\|} \hline \text { Rev } \\ \hline \text { A } \end{array}$ | $\begin{gathered} \text { Number } \\ \hline \text { N/A } \end{gathered}$ | $\begin{aligned} & \hline \text { Date } \\ & \hline 5 / 1 / 90 \end{aligned}$ | $\begin{aligned} & \text { By } \\ & \hline \text { P.C. } \end{aligned}$ |  |  |  |  |
|  |  |  |  |  | OUTLINE |  |  |  |
|  | B | ECO 2102 | 11/29/00 | P.C. |  |  |  |  |
|  |  |  |  |  | Draw | By: | Date: | Sheet 2 of 2 |
|  |  |  |  |  | RWW |  | 11/29/00 |  |
| Information herein is believed accurate. Suitability not guaranteed. |  |  |  |  | DRF: | Date: | Approved: | $\begin{aligned} & \hline \text { Drawing \# } \\ & 9362-50 \end{aligned}$ |
|  |  |  |  |  |  | 3/27/90 | PC |  |

