

Package Code 4

Tolerances Except as Noted	Revisions				Impellimax			
	Rev	Number	Date	Ву	<u> </u>			
	Α	N/A	5/1/90	P.C.	OUTLINE			
	В	ECO 2102	11/29/00	P.C.	OUTLINE			
					Draw	n By:	Date:	Chast 4 of 2
					RWW		11/29/00	Sheet 1 of 2
Information herein is believed accurate. Suitability not					DRF:	Date:	Approved:	Drawing #
guaranteed.						3/27/90	PC	9362-50

PIN	CONNECTION	PIN	CONNECTION
1	-100V	22	NC
2	Output 1	21	NC
3	Dir 1	20	Rext/Cext 1
4	in 1	19	NC
5	Mode 1	18	NC
6	Ground	17	NC
7	Mode 2	16	NC
8	ln 2	15	NC
9	Dir 2	14	Rext/Cext 2
10	Output 2	13	NC
11	+5V +/5V	12	NC

Notes:

- 1) Steady-state output current capability is + 100 mA nominal into a cathodegrounded PIN diode.
- 2) Switching speed is 25 nsec max loaded with a shunt 1N914 diode and 100 pF.
- 3) Unit is output short-circuit protected. Negative supply range is from -20V to -100V maximum. +5V input is not over-voltage protected.
- 4) Channels are independent and provide TTL Exclusive-Nor logic function. Inputs are inverting when mode is held low.
- 5) Connect 22K parallel .01uF (typical values) between Rext/Cext and output.

Tolerances Except as Noted .x = +/05 .xx = +/01 .xxx = +/005 Dimensions in inches	Revisions				<u>Impellimax</u>			
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	Α	N/A	5/1/90	P.C.	OUTLINE			
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					Drawn By: RWW		Date:	Sheet 2 of 2
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