



<p>Tolerances Except as Noted .x = +/- .05 .xx = +/- .01 .xxx = +/- .005 Dimensions in inches</p>	<b>Revisions</b>				<b><u>Impellimax</u></b>	
					<b>OUTLINE</b>	
					Not to scale	Sheet 1 of 2
<p>Information herein is believed accurate. Suitability not guaranteed.</p>					Drawn By: PC	Date: 10/30/02
					DRF: 664	Approved: PC
				Drawing # <b>9739-50</b>		

PIN	CONNECTION	PIN	CONNECTION
1	+15 V	22	NC
2	Input True 1	21	Output 1A
3	Input Complement 1	20	NC
4	NC	19	Output 1B
5	NC	18	NC
6	Ground	17	NC
7	Input True 2	16	NC
8	Input Complement 2	15	Output 2A
9	NC	14	NC
10	NC	13	Output 2B
11	-40 to -60V (-48V nom)	12	NC

**Notes:**

- 1) Inputs are complementary and are nominally in the range of 0.4 to 5V. Input impedance to ground from either input is 1K minimum over the common-mode range of -10V to +10V. Line-to-line impedance is greater than 2K, such that an external common-mode resistor is required for balanced line termination. Threshold hysteresis is .1V minimum, and differential sensitivity is 400 mV maximum. Input will withstand a maximum input voltage of +/- 15V line to ground or line to line. Relative to Input True, Output A is inverting, and Output B is noninverting.
- 2) Switching speed is 1 Usec max into fast load diodes. Outputs are not current limited. Connect external current-limiting resistors between outputs and loads.
- 3) Positive outputs are capable of sourcing 200 mA and sinking 100 mA (with external current limiting).
- 4) Unit is reverse bias protected and contains internal .01uF bypass capacitors on both power supplies.
- 5) Channels are logically independent of each other.

Tolerances Except as Noted .x = +/- .05 .xx = +/- .01 .xxx = +/- .005 Dimensions in inches	Revisions				<b>Impellimax</b>		
	Information herein is believed accurate. Suitability not guaranteed.					<b>OUTLINE</b>	
							Sheet 2 of 2
					Drawn By: P.C.	Date: 10/30/02	Drawing #
					DRF: 664	Approved: P. C.	<b>9739-50</b>