



Package Code 4

<p>Tolerances Except as Noted .x = +/- .05 .xx = +/- .01 .xxx = +/- .005 Dimensions in inches</p>	Revisions				<u>Impellimax</u>		
					OUTLINE		
							Sheet 1 of 2
<p>Information herein is believed accurate. Suitability not guaranteed.</p>					Drawn By: P.C.	Date: 2/20/03	<p>Drawing # 9742-50</p>
					DRF: 637	Approved: PC	

PIN	CONNECTION	PIN	CONNECTION
1	- 15 V	22	NC
2	NC	21	NC
3	Inv Out 1	20	NC
4	Input 1	19	NC
5	NI Out 1	18	NC
6	Ground	17	NC
7	NC	16	NC
8	NC	15	NC
9	+ 5V TP	14	NC
10	NC	13	NC
11	+ 15 V	12	NC

Notes:

- 1) Unit has a single TTL compatible input and two outputs which are out of phase with each other.
- 2) Switching speed is 70 nsec typical into 1N914 loads. Outputs are not spiked.
- 3) Outputs provide +25 mA nominal into a +1V load. Negative output is for reverse bias of diodes only. Output switches to approximately -11 Volts in the reverse condition.
- 4) +15V supply is internally regulated to +5V nominal. -15V supply is internally dropped by a series 3.6V zener. Unit contains internal .01 uF bypass capacitors on internally generated voltages. Connect 1 uF (or larger) bypass cap to pin 9.
- 5) Expected operating temperature range is -57 to +85C. Unit will dissipate about 375 mW. Proper heatsinking is required.

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