



<p>Tolerances Except as Noted .x = +/- .05 .xx = +/- .01 .xxx = +/- .005 Dimensions in inches</p>	Revisions				<u>Impellimax</u>	
	A	ECO 2281	1/09/03	P.C.	OUTLINE	
					Not to scale	Sheet 1 of 2
					<p>Drawn By: P. C.</p> <p>DRF: 636</p>	<p>Date: 4/25/02</p> <p>Approved: P. C.</p>
<p>Information herein is believed accurate. Suitability not guaranteed.</p>					Drawing # 9743-50	

PIN	CONNECTION	PIN	CONNECTION
1	Output 1	22	Input 1
2	Output 2 (High Current)	21	Input 2
3	Output 3	20	Input 3
4	Output 4 (High Current)	19	Input 4
5	Output 5	18	Input 5
6	Output 6 (Hi Current)	17	Input 6
7	Ground	16	Input 7
8	Ground	15	+ 5 V
9	Ground	14	+ 5 V
10	- 5 V	13	+ 5 V
11	Output 7	12	+ 5 V

Notes:

- 1) Steady-state output current capability of channels 1, 3, 5, and 7 is + 70 mA minimum. Steady-state output current capability of High Current channels 2, 4, and 6 is + 400 mA minimum. Output current is set by external resistors. In normal operation, only one channel will be positive at any given time. Exceeding this loading could damage the unit.
- 2) PIN channels switching speed is 100 nsec max loaded with a shunt 1N914 diode. Output current for this test is +60 mA nominal for outputs 1, 3, 5, and 7, and the output current for this test is +360 mA nominal for outputs 2, 4, and 6.
- 3) Unit internally regulates positive output level to reduce output current sensitivity to +5V supply variation. Output positive voltage is typically +3.2V at the pin.
- 4) Inputs are intended to be driven by CMOS logic levels. Operation with bipolar TTL levels is possible, but compatibility over wide temperature ranges is not assured.
- 5) Unit contains internal .01uF bypass capacitors on both power supplies.
- 6) Channels are independent and inverting.

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