

Leadform: C176 Package Code K

Tolerances Except as Noted		REV	<u>Impellimax</u>		
.x = +/05 .xx = +/01 .xxx = +/005 Dimensions in inches Information herein is believed accurate. Suitability not guaranteed.	REV	DRF / ECO	DATE	REVISED BY	OUTLINE
	-	DRF 1398	01/12/2012	TM	Drawing Number:
	Α	ECO 3257	01/17/2012	YW	6140-50
			6140-50 is the outline drawing that applies to part number 6140 (optional suffix codes).		
			Approved: P.C.		
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PIN	CONNECTION	PIN	CONNECTION
1	NC	14	NC
2	Output 1	13	-V
3	Ground	12	Input 1
4	NC	11	Ground
5	Output 2	10	+5V
6	NC	9	Input 2
7	NC	8	-V

NOTES:

- 1) Channels are independent and TTL compatible. Output 1 is inverting (TTL hi provides -V out) and Output 2 is noninverting (TTL hi provides 0V out).
- 2) Switching speed is 25 nsec typical.
- 3) Unit does not contain internal bypass capacitors. For best operation, bypass both power supplies close to the power pins of the driver.
- 4) Like-labelled pins are not internally connected. Therefore, both pin 3 and pin 11 must be connected to ground, and both pin 8 and pin 13 must be connected to -V.
- 5) Logic inputs must be connected to valid logic levels, or pulled to 0V or +5V, for proper operation. Do not leave inputs unconnected.
- 6) Negative supply can be in the range of -10 to -40V.

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