

Package Code 1

Inf	Tolerances Except as Noted	Revisions			<u>lmpellimax</u>		
	.x = +/05 .xx = +/01 .xxx = +/005 Dimensions in inches				OUTLINE		
							Sheet 1 of 2
	nformation herein is believed accurate. Suitability not guaranteed.				Drawn By: P C	Date: 7/19/00	Drawing #
					DRF: 488	Approved: P C	9187-50

PIN	CONNECTION	PIN	CONNECTION
1	-V	14	NC
2	Output	13	NC
3	Ground	12	NC
4	NC	11	NC
5	Input A	10	NC
6	Input B	9	NC
7	+5 V	8	NC

## Notes:

- 1) Switching speed is 5 to 8 nsec into resistive loads.
- 2) Unit is reverse bias protected on the +5V supply and contains internal .01 bypass capacitors on both supplies.
- 3) Unit provides X-Nor function. Allow one input to float hi, or connect to + 5 V, for noninverting operation. Ground one input to cause the remaining input to be inverting.
- 4) Positive output current is +20 mA into +1.3 V load. Negative output is for reverse bias only. Output is spiked by 560 pF in series with 12 Ohms.
- 5) Negative supply can be in the range of -2 to -12 V .

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