



<p>Tolerances Except as Noted</p> <p>.x = +/- .05</p> <p>.xx = +/- .01</p> <p>.xxx = +/- .005</p> <p>Dimensions in inches</p>	Revisions				Impellimax		
	Rev	Number	Date	By	OUTLINE		
					<p>Drawn By:</p> <p>P.C.</p>	<p>Date:</p> <p>1/20/01</p>	Sheet 1 of 2
<p>Information herein is believed accurate. Suitability not guaranteed.</p>				<p>DRF:</p> <p>542</p>	<p>Date:</p> <p>1/20/01</p>	<p>Approved:</p> <p>P.C.</p>	

PIN	CONNECTION	PIN	CONNECTION
1	A	22	Logic 0
2	B	21	Logic 4
3	C	20	Input 0/4
4	Enable Low	19	Testpoint 0/4
5	Enable Hi	18	Output 0/4
6	Ground	17	Output 1
7	NC	16	Testpoint 1
8	NC	15	Output 2
9	Testpoint 3	14	Testpoint 2
10	Output 3	13	NC
11	-5V to -100V	12	+5V

Notes:

- 1) Enable Low must be low, and Enable Hi must be hi for decoding operation. If either enable is not satisfied, all outputs are forced positive.
- 2) Selected output goes negative, all others remain positive.
- 3) Switching speed is 500 nsec typical open circuit, 50 microseconds max.
- 4) Output current is +40 mA nominal into +1V load. Outputs do not provide current spikes.
- 5) Inputs are direct connection to 54F138 IC. A is least significant bit. Connect input 0/4 to logic 0 for 0 thru 3 decoding, or connect input 0/4 to logic 4 for 1 thru 4 decoding.
- 6) Unit contains .01 uF bypassing on positive supply, and 5000 pF bypassing on negative supply. Additional external power supply bypassing is recommended for best switching speed performance.

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