



Package Code 1

<p>Tolerances Except as Noted .x = +/- .05 .xx = +/- .01 .xxx = +/- .005 Dimensions in inches</p>	Revisions				<u>Impellimax</u>		
					OUTLINE		
							Sheet 1 of 2
	<p>Information herein is believed accurate. Suitability not guaranteed.</p>				<p>Drawn By: P C</p>		<p>Date: 7/19/00</p>
<p>DRF: 487</p>					<p>Approved: P C</p>		

PIN	CONNECTION	PIN	CONNECTION
1	-V	14	NC
2	Output 1	13	Output 2
3	Ground	12	NC
4	NC	11	NC
5	Input 1A	10	Input 2A
6	Input 1B	9	Input 2B
7	+5 V	8	NC

Notes:

- 1) Switching speed is 5 to 8 nsec into resistive loads.
- 2) Unit is reverse bias protected on the +5V supply and contains internal .01 bypass capacitors on both supplies.
- 3) Channels are independent and provide X-Nor function. For each channel, allow one input to float hi, or connect to + 5 V, for noninverting operation. Ground one input to cause the remaining input to be inverting.
- 4) Positive output current is +20 mA into +1.3 V load. Negative output is for reverse bias only. Output is spiked by 560 pF in series with 12 Ohms.
- 5) Negative supply can be in the range of -2 to -12 V .

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	Information herein is believed accurate. Suitability not guaranteed.					Drawn By: P C Date: 7/19/00	Drawing # 9287-50
					DRF: 487 Approved: P C		