



Package Code 4

<p>Tolerances Except as Noted .x = +/- .05 .xx = +/- .01 .xxx = +/- .005 Dimensions in inches</p>	Revisions				<u>Impellimax</u>		
	Rev	Number	Date	Bv	OUTLINE		
					Drawn RWW	Date: 7/28/00	Sheet 1 of 2
					DRF	Date: 7/28/00	Approved: PC
Information herein is believed accurate. Suitability not							Drawing # 9312-50

PIN	CONNECTION	PIN	CONNECTION
1	+5V	22	GROUND
2	IN A	21	VEE APPLIED
3	IN B	20	R SET
4	ENABLE	19	NC
5	NC	18	NC
6	$\overline{\text{OUT 4}}$	17	$\overline{\text{OUT 1}}$
7	OUT 4	16	OUT 1
8	NC	15	NC
9	$\overline{\text{OUT 3}}$	14	$\overline{\text{OUT 2}}$
10	OUT 3	13	OUT 2
11	VEE INT	12	GROUND

Notes:

- 1) Unit accepts two bit binary TTL inputs (pin 2 and pin 3) and select one of four channels. Enable (pin 4) must be high for decoding operation.
- 2) Outputs are compatible with standard GaAs MMIC switch bias requirements.
- 3) Switching speed is 60 nsec max.

	INPUT		CH 1		CH 2		CH 3		CH 4		
PIN #	2	3	4	16	17	13	14	10	9	7	6
	0	0	1	*-V	0	0	-V	0	-V	0	-V
	1	0	1	0	-V	*-V	0	0	-V	0	-V
	0	1	1	0	-V	0	-V	*-V	0	0	-V
	1	1	1	0	-V	0	-V	0	-V	*-V	0
	X	X	0	0	-V	0	-V	0	-V	0	-V

* = Selected Channel

Supply Current : +5V typical, 35 mA / -V typical , 20 mA

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	Rev	Number	Date	By				
Information herein is believed accurate. Suitability not guaranteed.					Drawn By: RWW	Date: 7/28/00	Sheet 2 of 2	
					DRF: 493	Date: 7/28/00		
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