

FOUR OUTPUT DECODED PIN DRIVER WITH ENABLE

Substrate Size: .321 by .325.

Switching speed 50% TTL to zero crossing, antiparallel diodes: 30-40 ns.

Rise/Fall time (+.8V to -.8V): 15-20 ns. Float Enable Hi for decoding operation.

Outputs decode from 00 thru 11 (0 to 3). Ain is LSB $\,$

Output current +/-20mA +/- -3mA, independent of -V

Use on-board negative side series zener for negative supplies that exceed -12V.

For negative supply voltages less than -12V, connect to gold run upon which zener sits.

Quiescent current +60mA, -5mA max.

Inputs A, B and C are reverse protected. Supplies have reverse voltage protection

	Tolerances Except as Noted .x = +/05 .xx = +/01 .xxx = +/005 Dimensions in inches	Revisions				<u>lmpellimax</u>		
		Α	ECO 2069	8/2/00	PC	OUTLINE		
						Not to Scale Sheet 1 of 1		
	Information herein is believed accurate. Suitability not					Drawn By: PC	Date: 5/6/92	Drawing #
	guaranteed.					DRF:	Approved: PC	9433-51