

| PIN | CONNECTION | PIN | CONNECTION |
|-----|------------|-----|------------|
| 1 | + 5 V | 22 | VEE |
| 2 | OUTPUT 1 | 21 | NC |
| 3 | INPUT 1A | 20 | NC |
| 4 | INPUT 1B | 19 | NC |
| 5 | OUTPUT 2 | 18 | NC |
| 6 | INPUT 2A | 17 | NC |
| 7 | INPUT 2B | 16 | NC |
| 8 | OUTPUT 3 | 15 | OUTPUT 4 |
| 9 | INPUT 3A | 14 | INPUT 4B |
| 10 | INPUT 3B | 13 | INPUT 4A |
| 11 | GROUND | 12 | GROUND |

Notes:

- 1) Channels are independent and provide Exclusive-Or logic function. Apply logic low to one input to cause the remaining input to be noninverting. Allow one input to float hi (or pull high) to cause the remaining input to be inverting.
- 2) Switching speed is 30 nsec typical, 70 nsec max into resistive loads. Outputs are not spiked.
- 3) Negative supply voltage can be in the range of -2 to -16 Volts. Negative output current increases with increasing negative voltage, and is nominally 50 mA per output when operated at +/-5V into a 1V load.
- 4) Unit is reverse bias protected and contains internal .01 uF bypass capacitors on both power supplies.
- 5) Ground pins are internally connected.

| Tolerances Except as Noted .x = +/- .05 .xx = +/- .01 .xxx = +/- .005 Dimensions in inches | Revisions | | | | Impellimax | | |
|--|---|--|--|--|-------------------|----------------|----------------|
| | Information herein is believed accurate. Suitability not guaranteed. | | | | | OUTLINE | |
| | | | | | Drawn By: P.C. | | |
| | | | | | DRF: 592 | Approved: P.C. | 9450-50 |