



<p>Tolerances Except as Noted .x = +/- .05 .xx = +/- .01 .xxx = +/- .005 Dimensions in inches</p>	Revisions				<u>Impellimax</u>		
					OUTLINE		
							Sheet 1 of 2
<p>Information herein is believed accurate. Suitability not guaranteed.</p>					Drawn By: P.C.	Date: 3/03/03	<p>Drawing # 9540-50</p>
					DRF: 677	Approved: P.C.	

PIN	CONNECTION	PIN	CONNECTION
1	+5 V	22	Ground
2	Input E1	21	NC
3	Output E2	20	NC
4	NC	19	NC
5	NC	18	NC
6	NC	17	NC
7	Output 3	16	NC
8	NC	15	NC
9	Output 2	14	Output 1
10	NC	13	NC
11	- 15 V	12	Ground

Notes:

1) Channels decode as shown below :

	<u>E1</u>	<u>E1</u>	<u>Out1</u>	<u>Out2</u>	<u>Out3</u>
L	L	-	-	+	
H	L	-	+	-	
L	H	+	-	-	
H	H	+	-	-	

2) Switching speed is 80 nsec typical into resistive loads. Outputs are not spiked.

3) Positive output current is +50 mA into +1V loads. Negative output is for back bias of diodes only.

4) Unit contains internal .01 uF bypass capacitors on both power supplies.

5) E1 input is direct connection to a single F gate. E2 input is conected to a quantity of 4 F logic inputs.

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