

	Tolerances Except as Noted .x = +/05 .xx = +/01 .xxx = +/005 Dimensions in inches	Revisions				<u>lmpellimax</u>		
د.		Α	ECO 2151	4/11/01	P.C.	OUTLINE		
						Not to scale		Sheet 1 of 2
	ormation herein is elieved accurate. Suitability not					Drawn By [·] P.C.	Date: 3/23/01	Drawing #
	guaranteed.					DRF: 553	Approved: P.C.	9637-50

PIN	CONNECTION	PIN	CONNECTION
1	+ 5 V	22	Ground
2	Output 1	21	Input 4B
3	Input 1A	20	Input 4A
4	Input 1B	19	Output 4
5	Output 2	18	Input 5B
6	Input 2A	17	Input 5A
7	Input 2B	16	Output 5
8	Output 3	15	Input 6B
9	Input 3A	14	Input 6A
10	Input 3B	13	Output 6
11	Ground	12	- 12 V

Notes:

- 1) Channels are independent and provide Exclusive-Or logic function. Apply logic low to one input to cause the remaining input to be noninverting. Allow one input to float hi (or pull high) to cause the remaining input to be inverting.
- 2) Output 4 and Output 5 output resistance (internal to part) are both set for 38 ohms. All other channels have 57 ohm output resistance. Outputs are not spiked.
- 3) Switching speed is 50 nsec typical, 90 nsec max into resistive loads.
- 4) Negative supply can be in the range of -2 to -12V nominal. Negative output current increases with increasing negative voltage.
- 5) Unit is reverse bias protected and contains internal .01 uF bypass capacitors on both power supplies.
- 6) Ground pins are internally connected.

Excep .x .xx .xxx Dim	Tolerances	Revisions				<u>lmpellimax</u>		
	ccept as Noted .x = +/- .05 .xx = +/- .01 .xx = +/- .005 Dimensions	A	ECO 2151	4/11/2001	P.C.	OUTLINE		
	in inches							Sheet 2 of 2
b	nformation herein is believed accurate. Suitability not guaranteed.					Drawn By: P.C.	Date: 3/23/01	Drawing #
						DRF: 553	Approved: P.C.	9637-50