

Package Code 3

		Revisions				<u>lmpellimax</u>			
Tolerances Except as Note	Rev	Number	Date	Ву	<u> </u>			<u> </u>	
.x = +/- .05 .xx = +/- .01 .xxx = +/- .005	05 01 A	ECO 2098	11/16/00	PC	OUTLINE				
Dimensions in inches									
					Drawn By: RWW		Date: 11/16/00	Sheet 1 of 2	
Information herein i believed accurate. Suitability not	-				DRF:	Date:	Approved:	Drawing #	
guaranteed.					-	12/11/96	PC	9694-50	

PIN	CONNECTION	PIN	CONNECTION
1	-V	22	NC
2	Output 1	21	Output 4
3	NC	20	NC
4	Input 1	19	Input 4
5	NC	18	NC
6	Ground	17	+ 5 V
7	NC	16	NC
8	Input 2	15	Input 3
9	NC	14	NC
10	Output 2	13	Output 3
11	-V	12	NC

Notes:

- 1) Channels are TTL compatible, independent and inverting.
- 2) Switching speed is 5 nsec typical into 1N914 loads.
- 3) Unit contains internal .01 uF bypass capacitors.
- 4) Like-labelled pins are not internally connected.
- 5) Output current is +/-20 mA nominal. Outputs are spiked by 560 pF capacitor. Negative output current increases with increasing negative supply voltage.
- 6) Negative supply can be from -2 to -10V.

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