



Package Code 4

<p>Tolerances Except as Noted .x = +/- .05 .xx = +/- .01 .xxx = +/- .005 Dimensions in inches</p>	Revisions				<b>Impellimax</b>		
	Rev	Number	Date	By	<b>OUTLINE</b>		
	A	ECO 2103	11/29/00	P.C.			
<p>Information herein is believed accurate. Suitability not guaranteed.</p>					Drawn By: RWW	Date: 11/29/00	Sheet 1 of 2
					DRF:	Date:	Approved:
					499	8/24/00	PC

PIN	CONNECTION	PIN	CONNECTION
1	Test Point 1	22	VEE (-5V to -15V)
2	Output 1	21	NC
3	NC	20	NC
4	Input 1	19	NC
5	Ground	18	NC
6	Mode 1	17	NC
7	Mode 2	16	NC
8	Input 2	15	NC
9	NC	14	NC
10	Output 2	13	NC
11	Test Point 2	12	VCC (+5V +/- 0.5V)

**Notes:**

- 1) Reverse Voltage Protection.
- 2) Regular outputs provide 10 nsec maximum (6 nsec typical) turn on time and 12 nsec maximum (8 nsec typical) turn off time into resistive loads.
- 3) Testpoint switching speed is 40 nsec typical (positive-going) and 50 nsec typical (negative-going) when driving short-lifetime RF diodes in shunt configuration.
- 4) Regular outputs provide +10mA/-35mA output current into antiparallel diode loads.
- 5) When using 1N4148 diode grounded cathode loads at two testpoints simultaneously, positive current output is to be 110mA / +/-15mA. Forward voltage drop of diodes to be .93V +/- .03V.
- 6) Regular outputs are useful for pulse repetition rates to 20 MHz when using short lifetime diodes, with +/-5V supply voltages.
- 7) Testpoints are useful for pulse repetition rates to 2 MHz (5 MHz typical) when using short lifetime diodes. Testpoints are capable of providing 200 nsec positive pulses at a 20% Duty cycle.
- 8) Inputs are inverting when mode control is held low (0V) and non-inverting when mode control is held high (+5V).
- 9) Regular outputs have internal spiking capacitors to provide large current spikes during the time that the driver is switching states.

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