



Two Channel PIN Driver, High Speed

Substrate Size : .480 inch by .230 inch

- 1) Switching speed is 10 nsec max typical turn on time and 12 nsec typical turn off time into resistive loads.
- 2) Outputs provide +10 mA / - 35 mA into antiparallel diode loads when operated at +5V / -15V.
- 3) Unit contains internal .01 uF bypassing on both power supplies.
- 4) Channels are inverting when mode control is low, and noninverting when mode control is high.
- 5) Backside is bare Alumina. Substrate thickness is .010 inch, overall thickness is less than .060 inch.
- 6) Outputs provide current spikes during switching. Testpoints are not current limited.
- 7) Unit is reverse bias protected. +5V supply connection is made to the top (anode) of a large-area schottky diode. Top metallization of diode is aluminum.
- 8) Logic inputs connect to F-series logic gate through a series current-limiting resistor. Logic inputs float to Logic 1 condition when left unconnected.

Tolerances Except as Noted .x = +/- .05 .xx = +/- .01 .xxx = +/- .005 Dimensions in inches	Revisions				<u>Impellimax</u>		
	A	ECO 2156	4/26/2001	PC	OUTLINE		
							Sheet 1 of 1
Information herein is believed accurate. Suitability not guaranteed.					Drawn By: P.C.	Date: 4/9/01	Drawing # 9718-51
					DRF: 558	Approved: P.C.	