

| PIN | CONNECTION | PIN | CONNECTION |
| :---: | :---: | :---: | :---: |
| 1 | +15 V | 22 | NC |
| 2 | Input True | 21 | Output A |
| 3 | Input Complement | 20 | NC |
| 4 | NC | 19 | NC |
| 5 | NC | 18 | NC |
| 6 | Ground | 17 | NC |
| 7 | Ground | 16 | NC |
| 8 | Status | 15 | NC |
| 9 | NC | 14 | NC |
| 10 | Ground | 13 | Output B |
| 11 | -40 to $-60 V$ (-48V nom) | 12 | NC |

Notes:

1) Inputs are complementary and are nominally in the range of 0.4 to 5 V . Input impedance to ground from either input is 1 K minimum over the common-mode range of -10 V to +10 V . Line-to-line impedance is greater than 2 K , such that an external common-mode resistor is required for balanced line termination. Threshold hysteresis is .1 V minimum, and differential sensitivity is 400 mV maximum. Input will withstand a maximum input voltage of $+/-15 \mathrm{~V}$ line to ground or line to line. Relative to Input True, Output A is inverting, and Output B is noninverting.
2) Switching speed is 1 Usec max into Sage load diodes. Outputs are spiked.
3) Positive output currents are $+\mathbf{7 0} \mathrm{mA}$ nominal into +.87 V loads. Negative outputs are capable of sinking 10 mA (with external current limiting).
4) Unit is reverse bias protected and contains internal .01 uF bypass capacitors on both power supplies.
5) The Status output reports the operational status of the driver. It is a bilevel discrete signal with the following characteristics:

Fault Status : Open Circuit > 75 Kohms
Fault Conditions : If positive current is less than $\mathbf{3 0 \%}$ of nominal currents for A and B If negative voltage is less negative than -30 V , or not present.
Fault Blanking : Fault Status reporting shall be blanked by 2 microseconds nominal delay following switching.
6) Total supply current is 95 mA max at +15 V and 8 mA at -48 V .

| Tolerances Except as Noted $\begin{array}{rr} . x=+/- & .05 \\ . x x=+/- & .01 \end{array}$ $. x x x=+/-.005$ <br> Dimensions in inches | Revisions |  |  |  | Impellimax |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
|  | A | ECO 2164 | 5/8/01 | PC | OUTLINE |  |  |
|  |  |  |  |  |  |  | Sheet 2 of 2 |
| Information herein is believed accurate. Suitability not guaranteed |  |  |  |  | Drawn By: P.C. | Date: 5/7/01 | Drawing \# |
|  |  |  |  |  | DRF: 569 | Approved: P . C. | 9728-50 |

