

DRF:

626

Approved:

P.C.

9816-50

guaranteed.

PIN	CONNECTION	PIN	CONNECTION		
1	+ 5 V	22	NI Output 1		
2	Logic Input	21	NI Output 2		
3	NC	20	NI Output 3		
4	NC	19	NI Output 4		
5	Inv Output 8	Inv Output 8 18 NI Output			
6	Ground	17	NI Output 6		
7	Inv Output 7	16	NI Output 7		
8	Inv Output 6	15	NI Output 8		
9	Inv Output 5	14	Inv Output 1		
10	Inv Output 4	13	Inv Output 2		
11	Vee	12	Inv Output 3		

## Notes:

- 1) Switching speed into resistive loads is 10 nsec. Outputs are spiked.
- 2) Unit contains internal .01 uF bypass caps on power supplies.
- 3) Positive supply is reverse bias protected.
- 4) Noninverting outputs go positive for TTL hi input, while Inverting outputs go negative.
- 5) Negative supply range is 4 to 12 V. Negative output current increases with increasing negative voltage.
- 6) Output current per output is +/- 20 mA nominal into +/- 1V loads when operated at +5V/-5V.

Tolerances Except as Noted	Revisions			<u>lm</u> p <u>ellimax</u>				
.x = +/05 .xx = +/01 .xxx = +/005 Dimensions in inches					OUTLINE			
								Sheet 2 of 2
Information herein is believed accurate. Suitability not					Drawn I	By: P.C.	Date: 3/04/02	Drawing #
guaranteed.					DRF:	626	Approved: P.C	9816-50