



Package Code 2

<p>Tolerances Except as Noted .x = +/- .05 .xx = +/- .01 .xxx = +/- .005 Dimensions in inches</p>	<b>Revisions</b>				<b><u>Impellimax</u></b>		
	A	ECO 2132	2/15/01	P.C.	<b>OUTLINE</b>		
							Sheet 1 of 2
<p>Information herein is believed accurate. Suitability not guaranteed.</p>					Drawn By: J.P.	Date: 2/11/99	Drawing #
					DRF: 278	Approved: P. C.	<b>AP17-52</b>

PIN	CONNECTION	PIN	CONNECTION
1	- V	14	BP 1
2	Output	13	BP 2
3	Ground	12	BP 3
4	Input	11	BP 4
5	Comp	10	BP 5
6	N C	9	BP 6
7	+ V	8	BP 7

**Notes:**

- 1) Input Impedance is 15 K normal.
- 2) Unit contains internal .01 bypass capacitors on both supplies.
- 3) Linearizer sources positive current, concave-up transfer function.
- 4) Total delta between supply voltages should be 35 V or less ("C" voltage code), 50 V or less ("D" voltage code), or 110 V or less ("E" voltage code).
- 5) Uncompensated setting time into resistive load is typically 1 usec ("C" voltage code) or 2 usec ("D" and "E" voltage codes), depending on conditions. Connect a small value capacitor (or cap/resistor series combination) between the input the compensation pin for output spiking. Faster settling is possible using the LinSpike capability of the APD series linearizers.
- 6) Negative supply can be in the range of - 2 to - 16 V.

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