


Leadform: C176
Package Code K

Tolerances Except as Noted .x = +/- .05 .xx = +/- .01 .xxx = +/- .005 Dimensions in inches	REVISIONS				Impellimax
	REV	DRF / ECO	DATE	REVISED BY	OUTLINE
	-	DRF1310	02/17/2011	TM	Drawing Number: AP17-55
Information herein is believed accurate. Suitability not guaranteed.					AP17-55 is the semi-generic outline drawing that applies to the AP17 series.
					Approved: P.C.
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PIN	CONNECTION	PIN	CONNECTION
1	VEE	14	BP 1
2	Output	13	BP 2
3	Ground	12	BP 3
4	Input	11	BP 4
5	Comp	10	BP 5
6	NC	9	BP 6
7	+V	8	BP 7

NOTES:

- 1) Input impedance is 15K nominal.
- 2) Unit contains internal .01uF bypass capacitors on both supplies.
- 3) Linearizer sources positive current and contains 7 breakpoints.
- 4) Total delta between supply voltages should be 18V or less ("A" voltage code), or 25V or less ("B" voltage code).
- 5) Uncompensated settling time into resistive load is nominally 100 nsec, depending on conditions. Connect a small value capacitor (or cap/resistor series combination) between the input and compensation pins for output spiking.

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