



<p>Tolerances Except as Noted</p> <p>.x = +/- .05 .xx = +/- .01 .xxx = +/- .005</p> <p>Dimensions in inches</p>	Revisions				<u>Impellimax</u>		
					OUTLINE		
							Sheet 1 of 2
<p>Information herein is believed accurate. Suitability not guaranteed.</p>					Drawn By: P. C.	Date: 6/28/00	<p>Drawing # GS3-52</p>
					DRF: 483	Approved: P. C.	

PIN	CONNECTION	PIN	CONNECTION
1	-5 V to -18V	22	NC
2	NC	21	Out 1 NI
3	Rset	20	Out 1 Inv
4	Ground	19	Out 2 NI
5	In 1	18	Out 2 Inv
6	In 2	17	Out 3 NI
7	In 3	16	Out 3 Inv
8	NC	15	NC
9	NC	14	NC
10	NC	13	NC
11	+5V	12	-V Int

Notes:

- 1) Channels are independent and TTL compatible. Logic "0" is defined as 0.8 V max, logic "1" is 1.4 to 5 V max. Logic low TTL current is 0.5 mA max.
- 2) Noninverting outputs are at ground for TTL hi, and inverting outputs are at negative.
- 3) Switching speed is 500 nsec typical.
- 4) Unit contains internal .01 uF bypass capacitors.
- 5) Connect resistor (in the range of 2K to 10K) from Rset (pin 3) to -V (pin 1) to set negative output voltage. Alternatively, apply -5 to -8 V directly to - V Int (pin 12).

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