

PIN	CONNECTION	PIN	CONNECTION
1	- 28 V	22	NC
2	Out 1	21	NC
3	Testpoint 1	20	NC
4	In 1A	19	NC
5	In 1B	18	NC
6	Ground	17	NC
7	In 2B	16	NC
8	In 2A	15	NC
9	Testpoint 2	14	NC
10	Out 2	13	NC
11	+5V	12	NC

Notes:

- 1) Inputs are X-Nor TTL. Apply logic hi to one input to cause the remaining input to be noninverting. Apply logic low to cause the remianing input to be inverting.
- 2) Switching speed is 30 nsec typical into shunt 1N914 loads. Outputs are spiked.

3) Outputs are not short-circuit protected.

4) Unit is not reverse bias protected.

5) Unit contains internal .01 uF bypass capacitors on both power supplies.

	Revisions				Impellimax				
Tolerances Except as Noted	Rev	Number	Date	Ву					
.x = +/05 .xx = +/01 .xxx = +/005	Α	ECO 2154	4/23/01	PC	OUTLINE				
Dimensions in inches									
					Drawn By: PC		Date: 4/23/01	Sheet 2 of 2	
Information herein is believed accurate. Suitability not					DRF:	Date:	Approved:	Drawing #	
guaranteed.					-	2/9/96	PC	MF2-54	