

Tolerances Except as Noted .x = +/05 .xx = +/01 .xxx = +/005 Dimensions in inches	Revisions				<u>lmpellimax</u>		
	Α	ECO 2070	8/3/00	PC	OUTLINE		
							Sheet 1 of 2
Information herein is					Drawn By:	Date:	Drawing #
believed accurate. Suitability not guaranteed.					P.C.	11/2/99	
					DRF: 385	Approved:	NS840A5-50

PIN	CONNECTION	PIN	CONNECTION	
1	+ 5 V	22	- V	
2	Input 1	21	Output 8	
3	Output 1	20	Input 8	
4	Input 2	19	Output 7	
5	Output 2	18	Input 7	
6	Input 3	17	Output 6	
7	Output 3	16	Input 6	
8	Input 4	15	Output 5	
9	Output 4	14	Input 5	
10	NC	13	NC	
11	Ground	12	Ground	

Notes:

- 1) Channels are independent and noninverting.
- 2) Switching speed is 60 nsec typical into resistive loads. Outputs are not spiked.
- 3) Negative output current increases with increasing negative voltage.
- 4) Unit contains internal .01 uF bypass capacitors on both power supplies.
- 5) Quiescent +5V current is 40 mA max. Negative quiescent current is less than 5 mA for each negative out channel.
- 6) Operating temperature range is -55C to +125 C. Storage temperature is +150 C maximum.

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