

Package Code 4

		Revisions				<u>lmpellimax</u>			
	Folerances cept as Noted	Rev	Number	Date	Ву	<u> </u>			<u> </u>
.x = +/- .05 .xx = +/- .01 .xxx = +/- .005	.x = +/05 xx = +/01	Α	ECO 2079	9/29/00	PC	OUTLINE			
	imensions in inches					OUTLINE			
						Draw RW	n By: /W	Date: 9/29/00	Sheet 1 of 2
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gua	aranteed.					298	3/5/99	PC	NX4-52

PIN	CONNECTION	PIN	CONNECTION
1	NC	22	NC
2	Output 1	21	Output 4
3	TP 1	20	TP 4
4	NC	19	NC
5	in 1	18	In 4
6	Ground	17	+ 5 V
7	ln 2	16	In 3
8	NC	15	NC
9	TP 2	14	TP 3
10	Output 2	13	Output 3
11	NC	12	NC

Notes:

- 1) In the positive output condition, outputs provide positive output current and current spikes. In the other condition, outputs provide current spikes during transitions, and pull to ground with a 2K nominal impedance to assure complete extinction of load diode conduction in this state.
- 2) Channels are independent and noninverting.
- 3) Switching speed is 5 to 8 nsec maximum into 1N914 loads.
- 4) Unit contains an internal .01 uF bypass capacitor on the +5V supply.

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					OUTLINE			iC .	
					Draw		Date: 9/29/00	Sheet 2 of 2	
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