



<p>Tolerances Except as Noted .x = +/- .05 .xx = +/- .01 .xxx = +/- .005 Dimensions in inches</p>	<b>Revisions</b>				<b><u>Impellimax</u></b>		
	A	ECO	5/10/1991	P.C.	<b>OUTLINE</b>		
	B	ECO 2089	10/23/2000	P.C.			
<p>Information herein is believed accurate. Suitability not guaranteed.</p>					Not to scale	Sheet 1 of 2	
					<p>Drawn By: P.C.</p>	<p>Date: 9/6/89</p>	Drawing #
					<p>DRF: NA</p>	<p>Approved: P.C.</p>	<b>XS6-50</b>

PIN	CONNECTION	PIN	CONNECTION
1	+ 5 V	22	Ground
2	Output 1	21	Input 4B
3	Input 1A	20	Input 4A
4	Input 1B	19	Output 4
5	Output 2	18	Input 5B
6	Input 2A	17	Input 5A
7	Input 2B	16	Output 5
8	Output 3	15	Input 6B
9	Input 3A	14	Input 6A
10	Input 3B	13	Output 6
11	Ground	12	- V

**Notes:**

- 1) Channels are independent and provide Exclusive-Or logic function. Apply logic low to one input to cause the remaining input to be noninverting. Allow one input to float hi (or pull high) to cause the remaining input to be inverting.
- 2) Switching speed is 30 nsec typical, 70 nsec max into resistive loads. Outputs are not spiked.
- 3) Negative output current increases with increasing negative voltage.
- 4) Unit is reverse bias protected and contains internal .01 uF bypass capacitors on both power supplies.
- 5) Ground pins are internally connected.

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